Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	10/627333	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/04/26 13:30
S13	238	(714/742).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S11	208	(714/741).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S10	377	(714/734).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S9	3307	(714/724).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S8	467	(714/33).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S7	204	(714/32).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S6	293	(714/31).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S5	812	(714/30).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28

S4	79	(714/29).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S3	188	(714/28).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S2	202	(714/27).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:28
S19	31	(electronic adj design adj automation) and (ip adj core\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:57
S18	359	(electronic adj design adj automation) and core\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:57
S17	981	((ip (intellectual adj propert\$3)) adj core\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:57
S15	2629	((ip (intellectual adj propert\$3)) adj core\$1) (electronic adj design adj automation)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 10:57
S20	7	(electronic adj design adj automation) and (intellectual adj property adj core\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 13:12
S22	13185	((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 13:36

S24	1	("6405335").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:08
S23	243	(S2 S3 S5 S6 S9 S10 S11) and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 14:08
S27	10	(("5321828") or ("5329471") or ("5426741") or ("5687311") or ("5875294") or ("5951696") or ("6012142") or ("6131174") or ("6587967") or ("6601189")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:09
S26	1	("6530047").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:09
S25	5	(("5850511") or ("5903718") or ("5974532") or ("5995744") or ("6260160")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:09
S29	4	(("6021511") or ("6158032") or ("6907548") or ("6918058")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:10
S28	8	(("5084814") or ("5491793") or ("5555287") or ("5644703") or ("5983017") or ("6571356") or ("6587966") or ("6757846")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:10
S30	12	(("20040114524") or ("20050149800") or ("5155732") or ("5657328") or ("5768152") or ("5862152") or ("6427217") or ("6640324") or ("6643812") or ("6662313") or ("6711684") or ("6779142")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:11
S31	13	(("20030037277") or ("20030204782") or ("20030204783") or ("4191996") or ("5123016") or ("5748640") or ("5961653") or ("5991898") or ("6154855") or ("6201733") or ("6421283") or ("6603691") or ("6907548")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 14:12
S38	3307	(714/724).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:23

S37	293	(714/31).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:23
S36	812	(714/30).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:23
S35	188	(714/28).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:23
S34	202	(714/27).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:23
S33	13185	((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:23
S32	231796	((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)		OR	OFF	2006/05/30 17:23
S43	1	("6530047").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:24
S42	5	(("5850511") or ("5903718") or ("5974532") or ("5995744") or ("6260160")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:24

S41	1	("6405335").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:24
S40	208	(714/741).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:24
S39	377	(714/734).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 17:24
S46	8	(("5084814") or ("5491793") or ("5555287") or ("5644703") or ("5983017") or ("6571356") or ("6587966") or ("6757846")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:27
S45	10	(("5321828") or ("5329471") or ("5426741") or ("5687311") or ("5875294") or ("5951696") or ("6012142") or ("6131174") or ("6587967") or ("6601189")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:27
S48	12	(("20040114524") or ("20050149800") or ("5155732") or ("5657328") or ("5768152") or ("5862152") or ("6427217") or ("6640324") or ("6643812") or ("6662313") or ("6711684") or ("6779142")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:28
S47	4	(("6021511") or ("6158032") or ("6907548") or ("6918058")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:28
*	13	(("20030037277") or ("20030204782") or ("20030204783") or ("4191996") or ("5123016") or ("5748640") or ("5961653") or ("5991898") or ("6154855") or ("6201733") or ("6421283") or ("6603691") or ("6907548")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/30 17:29
S50	9	("4875003" "5544174" "5640404" "5862152" "6000051").PN. OR ("6405335"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/30 17:56

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S53	15	("4698588" "5029166" "5412260" "5513187" "5592493" "5631911" "5673276" "5805609" "6115763" "6223315" "6266793" "6405335" "6408413" "6425100" "6425101").PN. OR ("7013415"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/30 18:22
S54	11	("20020184562" "5790871" "5978937" "6065078" "6115763" "6311302" "6324614" "6601189" "6687865" "6691266").PN. OR ("6918058").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/30 18:32
S52	60	(S42 S43 S45 S46 S47 S48 S49 S50) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 18:38
S56	24	("4855669" "4947395" "5115435" "5150044" "5418470" "5426626" "5442640" "5448575" "5448576" "5596585" "5610927" "5617431" "5644580").PN. OR ("5805609"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/30 18:57
S57	19	("5396501" "5544174" "5673276" "5744949" "5862152" "5900753").PN. OR ("6223315").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/30 19:00
S55	24	((S53 S54) not (S42 S43 S45 S46 S47 S48 S49 S50)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/30 19:01
S59	13	("5627841" "5680406" "5710867" "5768289" "5847561" "5960008" "6000051" "6079039" "6122762" "6134675" "6189128" "6223315" "6324662").PN. OR ("6898748"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 09:49
S60	16	("4493045" "4710931" "4728883" "4855670" "5115435" "5329471" "5477545").PN. OR ("5617431"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:00

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S64	50	("4357703" "4602210" "4710931" "4857835" "4872169" "5001713" "5056093" "5056094" "5329471" "5428622" "5453992" "5483518" "5495487" "5526365" "5610530" "5610826" "5623500" "5627839" "5677915" "5687179" "5701307" "5710779" "5715254" "5715255" "5744949" "5802270" "5847561" "5862152" "5887004" "5905738" "5907562" "6046600" "6073254" "6115763" "6199182" "6223315" "6242269" "6311302").PN. OR ("6560734").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:14
S63	6	("5355369" "5544174" "5744949" "5862152" "6115763" "6223315").PN. OR ("6643810").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:14
S76	19	("5396501" "5544174" "5673276" "5744949" "5862152" "5900753").PN. OR ("6223315").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:16
S75	24	("4855669" "4947395" "5115435" "5150044" "5418470" "5426626" "5442640" "5448575" "5448576" "5596585" "5610927" "5617431" "5644580").PN. OR ("5805609"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:16
S74	11	("20020184562" "5790871" "5978937" "6065078" "6115763" "6311302" "6324614" "6601189" "6687865" "6691266").PN. OR ("6918058").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:16

S73	15	("4698588" "5029166" "5412260" "5513187" "5592493" "5631911" "5673276" "5805609" "6115763" "6223315" "6266793" "6405335" "6408413" "6425100" "6425101").PN. OR ("7013415"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:16
S72	9	("4875003" "5544174" "5640404" "5862152" "6000051").PN. OR ("6405335"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 10:16
S71	13	(("20030037277") or ("20030204782") or ("20030204783") or ("4191996") or ("5123016") or ("5748640") or ("5961653") or ("5991898") or ("6154855") or ("6201733") or ("6421283") or ("6603691") or ("6907548")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16
S70	12	(("20040114524") or ("20050149800") or ("5155732") or ("5657328") or ("5768152") or ("5862152") or ("6427217") or ("6640324") or ("6643812") or ("6662313") or ("6711684") or ("6779142")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16
S69	4	(("6021511") or ("6158032") or ("6907548") or ("6918058")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16
S68	8	(("5084814") or ("5491793") or ("5555287") or ("5644703") or ("5983017") or ("6571356") or ("6587966") or ("6757846")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16
S67	10	(("5321828") or ("5329471") or ("5426741") or ("5687311") or ("5875294") or ("5951696") or ("6012142") or ("6131174") or ("6587967") or ("6601189")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16
S66	1	("6530047").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16
S65	5	(("5850511") or ("5903718") or ("5974532") or ("5995744") or ("6260160")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 10:16

S58	36	((S56 S57) not (S53 S54 S42 S43 S45 S46 S47 S48 S49 S50)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 10:16
581	377	(714/734).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 11:18
S80	3307	(714/724).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 11:18
S79	202	(714/27).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 11:18
S78	293	(714/31).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 11:18
S82	208	(714/741).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 11:49
S83	404	(703/15).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 13:51
S85	j.	("20010014957").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 13:52
S84	11	(("5805795") or ("5926622") or ("6061643") or ("6185518") or ("6263302") or ("6292765") or ("6301553") or ("6374367") or ("6389382") or ("6415396") or ("6567935")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 13:52

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S77	63	((S59 S60 S63 S64) not (S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 13:52
S87	19	("5546562" "5838948" "5943490" "5946472" "6052524" "6110220" "6178542").PN. OR ("6263302"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 14:09
588	19	("5202889" "5479414" "5600787" "5623499" "5654657" "5680332" "5729554").PN. OR ("6292765"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 14:11
S86	12	((S84 S85) not (S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 14:14
S91	15	("4725971" "4878179" "5214577" "5305437" "5650946" "5794005" "5862149" "5905883" "6012148" "6044211" "6097885" "6173241" "6226716" "6292765").PN. OR ("6510405").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 14:24
S92	16	("5202889" "5479414" "5539652" "55555270" "5600787" "5623499" "5630051" "5638381" "5654657" "5661661" "5680332" "5729554" "5862149" "6175946" "6292765" "6609229").PN. OR ("6885983").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 14:28
S93	80	("4306286" "4590581" "4633417" "4635218" "4644487" "4656580" "4782461" "4891773" "4901259" "4907180" "5053980" "5146460" "5325309" "5327361" "5329471" "5339262" "5353243" "5475832").PN. OR ("5546562").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 14:38

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S94	15	("5613087" "5671231" "5740353" "5859962" "5960457" "5999721" "6012157" "6131079" "6178533" "6226716" "6226760" "6230114" "6263302" "6446241" "6453437").PN. OR ("6567934"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 14:43
S89	36	((S87 S88) not (S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 14:43
S96	5	("5546562" "5838948" "6006022" "6009256" "6152612").PN. OR ("6298452"). URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 15:12
S97	38	("5539652" "5555270" "5600787" "5623499" "5630051" "5638381" "5654657" "5729554" "5862149" "6175946").PN. OR ("6175946" "6609229").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 15:30
S95	107	((S91 S92 S93 S94) not (S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 15:35
S10 0	14	("5404311" "5475605" "5475607" "5572717" "5764531" "6175946" "6209122" "6253361" "6324678" "6604227").PN. OR ("6457159" "6760894").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 15:46
S99	25	((S96 S97) not (S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 15:46
S98	7 	(application\$1 adj (interconnect\$3 interfac\$3) adj protocol\$1 adj monitor\$3)	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 15:47

S10 3	1212	(simulat\$4 emulat\$4 model\$4) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)) and ((interface interconnect connection protocol) near (monitor\$3 detect\$3 forward\$3 analy\$4 watch\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 15:53
510 5	174	((simulat\$4 emulat\$4 model\$4) same (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) megacell soc system-on-a-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))) and ((interface interconnect connection protocol) near (monitor\$3 detect\$3 forward\$3 analy\$4 watch\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 15:54
S10 7	14	(S106) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 15:55
S10	2	(simulat\$4 emulat\$4 model\$4) same (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)) same ((interface interconnect connection protocol) near (monitor\$3 detect\$3 forward\$3 analy\$4 watch\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 15:55

S10 2	4	(S98) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT;	OR	OFF	2006/05/31 15:55
S10	0	("2002/0138814").URPN.	IBM_TDB USPAT	OR	OFF	2006/05/31 16:04
S10 6	15	(simulat\$4 emulat\$4 model\$4) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)) same ((interface interconnect connection protocol) near (monitor\$3 detect\$3 forward\$3 analy\$4 watch\$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 16:14
S11 0	4	(("6269467") or ("6400173") or ("6631504") or ("6167363")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 16:16
S11 2	5	(("6725432") or ("6901562") or ("6879927") or ("6096092") or ("5911059")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 16:17
S11 1	6	(("6631504") or ("6775810") or ("6701474") or ("6654935") or ("6526561") or ("5933356")).PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2006/05/31 16:17
S10 1	13	(\$100 not (\$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 16:17
S11	12	((S108 S110 S111 S112) not (S100 S96 S97 S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 16:21

S11 8	466	(714/39).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:00
S12	52	("5535164" "5617531" "5619512" "5748640" "5801958" "5825785" "5903475" "5954824" "5963566" "5991898" "6003142" "6009256" "6094726" "6269467" "6304837" "6360353" "6532561").PN. OR ("6249893" "6678645").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 17:05
S11 4	180	(S82 not (S108 S110 S111 S112 S100 S96 S97 S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:18
S12 3	8	("5600579" "5838948" "6052524" "6240543" "6249893" "6269467").PN. OR ("6487699").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 17:32
S12 2	43	(\$121 not (\$82 \$108 \$110 \$111 \$112 \$100 \$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:36
S12 4	2	(\$123 not (\$121 \$82 \$108 \$110 \$111 \$112 \$100 \$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724" @pd<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:39

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S12 7	355	(S83 not (S123 S121 S82 S108 S110 S111 S112 S100 S96 S97 S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:41
S13	87	S83 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:45
S12 8	80	S83 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design adj reuse) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:45
S12 9	65	(S128 not (S123 S121 S82 S108 S110 S111 S112 S100 S96 S97 S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @prad<="20030724")		OR	⊙FF	2006/05/31 17:46
S13 1	87	S83 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:57

S13 2	71	(S131 not (S123 S121 S82 S108 S110 S111 S112 S100 S96 S97 S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 17:58
S13 7	812	(714/30).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:11
S13 6	98	(S133 not (S131 S123 S121 S82 S108 S110 S111 S112 S100 S96 S97 S91 S92 S93 S94 S87 S88 S84 S85 S59 S60 S63 S64 S75 S76 S73 S74 S65 S66 S67 S68 S69 S70 S71 S72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:11
S13	129	S80 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	EPO; JPO; DERWENT;	OR	OFF	2006/05/31 18:11
S14 0	14	("5321828" "5329471" "5426741" "5687311" "5812830" "5867644" "5875294" "5951696" "6012142" "6131174" "6587967" "6601189" "6665817" "6754852").PN. OR ("6948098").URPN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/05/31 18:17

						
S13 9	74	(\$138 not (\$133 \$131 \$123 \$121 \$82 \$108 \$110 \$111 \$112 \$100 \$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:20
S14 1	3	(\$140 not (\$138 \$133 \$131 \$123 \$121 \$82 \$108 \$110 \$111 \$112 \$100 \$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:22
S14 4	144	S78 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1) or (simulat\$4 emulat\$4 model\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:29
S14 3	89	(\$142 not (\$140 \$138 \$133 \$131 \$123 \$121 \$82 \$108 \$110 \$111 \$112 \$100 \$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:29

S14 2	103	S79 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1) or (simulat\$4 emulat\$4 model\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:29
514 5	122	(\$144 not (\$142 \$140 \$138 \$133 \$131 \$123 \$121 \$82 \$108 \$110 \$111 \$112 \$100 \$96 \$97 \$91 \$92 \$93 \$94 \$87 \$88 \$84 \$85 \$59 \$60 \$63 \$64 \$75 \$76 \$73 \$74 \$65 \$66 \$67 \$68 \$69 \$70 \$71 \$72)) and (@ad<="20030724"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/05/31 18:30
S13 8	99	S137 and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 09:59
138	21	((interfac\$3 interconnect\$3 bus sequenc\$3) near2 (monitor\$3)) with ((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 10:13

L35	1121	((interfac\$3 interconnect\$3 bus sequenc\$3) near2 (monitor\$3)) and ((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 10:13
L37	46	((interfac\$3 interconnect\$3 bus sequenc\$3) near2 (monitor\$3)) same (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 10:14
L40	932	((interfac\$3 interconnect\$3 ((on-chip onchip (on adj chip)) adj2 bus\$2)) near3 (monitor\$3)) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 12:58

L41	44	(US-20050021305-\$ or US-20020138814-\$ or US-20030051192-\$).did. or (US-6405335-\$ or US-6721913-\$ or US-5426741-\$ or US-6131174-\$ or US-7013415-\$ or US-6918058-\$ or US-6311302-\$ or US-6898748-\$ or US-6223315-\$ or US-6898748-\$ or US-6732304-\$ or US-66732304-\$ or US-66732304-\$ or US-6643810-\$ or US-6763485-\$ or US-6643810-\$ or US-6560734-\$ or US-6292765-\$ or US-6885983-\$ or US-6510405-\$ or US-6885983-\$ or US-65580-\$).did. or (US-6609229-\$ or US-6760894-\$ or US-6457159-\$ or US-6766499-\$ or US-6678645-\$ or US-6766499-\$ or US-678645-\$ or US-6249893-\$ or US-6678645-\$ or US-6487699-\$ or US-6553513-\$).did. or	US-PGPUB; USPAT; DERWENT	OR	OFF	2006/06/01 13:57
L42	0	(US-20050021305-\$).did. ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4)) and 41	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 13:58
L47	208	(714/741).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 13:59
L46	404	(703/15).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 13:59
L45	994	(703/14).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 13:59
L44	883	(703/13).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 13:59

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L39	40	37 and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 13:59
L51	132	34 and 43	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:00
L43	19088	((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:00
L52	0	(((interfac\$3 interconnect\$3 bus sequenc\$3) near2 (monitor\$3)) same 43) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:01
L53	0	51 and (44 45 46 47)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:02
L50	115	49 and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:02
L49	123	43 and (44 45 46 47)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:02

L57	20	49 and (((ip (intellectual adj	US-PGPUB;	OR	OFF	2006/06/01 14:03
		propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj	USPAT; EPO; JPO; DERWENT;			
		component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	IBM_TDB			
L56	125	51 and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:04
L58	18	57 and (@ad<="20030724" @pd<="20030724" @rlad<="20030724" @prad<="20030724")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:10
L59		(((interfac\$3 interconnect\$3 bus sequenc\$3) near2 (monitor\$3)) same ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4)) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)) and (simulat\$4 emulat\$4 model\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:12

L60	265	((interfac\$3 interconnect\$3 bus sequenc\$3) same ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4))) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)) and (simulat\$4 emulat\$4 model\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:13
L61	131		US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:18

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162	E24	((interfact 2 interconnect 2 bus	LIC-DCDLID:	OB	OFF	2006/06/01 14:10
L62	524	((interfac\$3 interconnect\$3 bus sequenc\$3) with ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4 analy\$7)) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1)) and (simulat\$4 emulat\$4 model\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:19
1.65	12	(((interfac\$3 interconnect\$3 bus buses busses) with ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8))) same (simulat\$4)) and (((ip (intellectual adj propert\$3)) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:34

						T-2
L64	37	(((interfac\$3 interconnect\$3 bus buses busses) with ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4 analy\$7)) same (simulat\$4)) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:34
L63	194	(((interfac\$3 interconnect\$3 bus sequenc\$3) with ((realtime real-time (real adj time) dynamic\$4 (event\$1 adj driven) event-driven eventdriven) with (check\$3 verif\$8 validat\$4 analy\$7))) same (simulat\$4 emulat\$4 model\$4)) and (((ip (intellectual adj propert\$3)) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-a-chip\$1 system-on-a-chip\$1 on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:34
L66	135	((interfac\$3 interconnect\$3) adj (monitor\$3)) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:55

L34	1172	((interfac\$3 interconnect\$3 bus sequenc\$3) near2 (monitor\$3)) and (((ip (intellectual adj propert\$3) embedded) adj core\$1) (electronic adj design adj automation) (virtual adj component\$1) (semiconductor adj (ip (intellectual adj propert\$3))) ((cell macro) adj librar\$3) (design\$1 near (reuse\$1 reusing)) megacell soc system-on-chip\$1 system-on-a-chip\$1 (system\$1 adj	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/06/01 14:55
		system-on-a-chip\$1 (system\$1 adj on adj2 chip\$1))				

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